



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Darrell Rinerson et al.

Attorney Docket No.: UNTYP015

Application No.: 10/612,733

Group: 2818

Filed: July 1, 2003

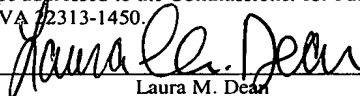
Confirmation No.: 5769

Title: LAYOUT OF DRIVER SETS IN A CROSS  
POINT MEMORY ARRAY

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on December 11, 2003 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

  
Laura M. Dean

**INFORMATION DISCLOSURE STATEMENT  
37 CFR §§1.56 AND 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached\*\*, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.


This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure

\*\* Copies of U.S. Patents and U.S. Published Applications are not attached.

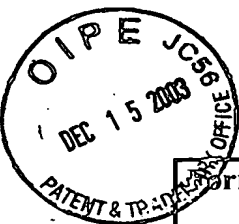
Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. UNTYP015).

Respectfully submitted,  
BEYER WEAVER & THOMAS, LLP



Morgan E. Malino  
Registration No. 41,177

P.O. Box 778  
Berkeley, CA 94704-0778



<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. UNTYP015 Applicant: Rinerson et al. Filing Date July 1, 2003	Application No.: 10/612,733  Group 2818
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#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	6,204,139	2001-03-20	Liu et al.			
	B	5,920,500	1999-07-06	Tehrani et al.			
	C	6,545,891	2003-04-08	Tringali et al.			
	D	6,504,753	2003-01-07	Scheuerlein et al.			
	E	6,483,736	2002-11-19	Johnson et al.			
	F	6,385,074	2002-05-07	Johnson et al.			
	G	6,351,406	2002-02-26	Johnson et al.			
	H	6,185,122	2001-02-06	Johnson et al.			
	I	6,034,882	2000-03-07	Johnson et al.			
	J	5,835,396	1998-11-10	Zhang			
	K	4,442,507	1994-04-10	Roesner			

#### U.S. Published Patent Applications

Examiner Initial	No.	Document No.	Publication Date	Inventor
	J	2002/0000597 A1	01/03/02	Okazawa
	K	2001/0023992 A1	09/27/01	Doll

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	L	Beck, A. et al., "Reproducible switching effect in thin oxide films for memory applications", Applied Physics Letters, Vol. 77, No. 1, 3 July 2000, 139-141.
	M	Rossel, C. et al., "Electrical current distribution across a metal-insulator-metal structure during bistable switching", Journal of Applied Physics, Vol. 90, No. 6, 15 September 2001, 2892-2898.
	N	Watanabe, Y. et al., "Current-driven insulator-conductor transition and nonvolatile memory in chromium-doped SrTiO <sub>3</sub> single crystals", Applied Physics Letters, Vol. 78, No. 23, 4 June 2001, 3738-3740.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.